



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

TAKASHI YAMADA ET AL.

: EXAMINER: CUONG Q. NGUYEN

SERIAL NO: 10/699,676

FILED: NOVEMBER 4, 2003

: GROUP ART UNIT: 2811

FOR: SEMICONDUCTOR DEVICE AND

MANUFACTURING METHOD THEREOF

ELECTION

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the election requirement dated August 20, 2004, Applicants elect Group 1, Claims 1-11, drawn to a semiconductor device, classified in class 257, subclass 350. Applicants reserve the right to file one or more divisional applications directed to the nonelected invention.

Accordingly, examination on the merits of Claims 1-11 is believed to be in order and an early and favorable action is respectfully requested.

Respectfully submitted,

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Docket No.: 244838US2S

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/699,676

Applicants: Takashi YAMADA, et al.

Filing Date: November 4, 2003

For: SEMICONDUCTOR DEVICE AND

MANUFACTURING METHOD THEREOF

Group Art Unit: 2811 Examiner: C. NGUYEN

SIR:

Attached hereto for filing are the following papers:

ELECTION

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

ØBLON, SPIVAK, McCLELLAND,

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